

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2397	SRAM and (power adj up)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/29 16:53
L2	413	1 and (word adj line\$1)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/29 16:54
L3	302	2 and (bit adj line\$1)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/29 16:54
L4	208	3 and charg\$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/29 16:54
L5	32	4 and (leakage adj current)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/29 16:57
L6	44	4 and (logic adj high)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/29 17:01
L7	29	4 and clamp\$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/29 17:04
L8	109	1 and 365/154	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/29 17:04
L9	6	8 and clamp\$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/29 17:05
L10	0	SRAM and (power adj on adj reset)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/29 17:06
L11	120	3 and isolat\$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/29 17:06
L12	14	11 and clamp\$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/29 17:07